



# Cryptographic Engineering

EPFL, LAUSANNE, SWITZERLAND

JUNE 19-23, 2017

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## MONDAY, June 19

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08:30 - 10:00 am	Introduction to Block Ciphers; DES and AES	C. Paar
10:30 - 12:00 pm	Lightweight Block Ciphers for RFIDs	C. Paar
01:30 - 03:00 pm	Modular Arithmetic Algorithms and Architectures	Ç. K. Koç
03:30 - 05:00 pm	Finite Fields Algorithms and Architectures	Ç. K. Koç

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## TUESDAY, June 20

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08:30 - 10:00 am	Specialized Hardware for Secret-Key Algorithms	I. Verbauwhede
10:30 - 12:00 pm	Introduction to PUFs (Physically Unclonable Functions)	I. Verbauwhede
01:30 - 03:00 pm	Public-Key Cryptography Algorithms and Protocols	Ç. K. Koç
03:30 - 05:00 pm	Public-Key Cryptography Software and Hardware Realizations	Ç. K. Koç

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## WEDNESDAY, June 21

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08:30 - 10:00 am	Trusted Computing Architectures, SSL and IPSec	P. Rohatgi
10:30 - 12:00 pm	Sub-Quadratic Multiplication for Cryptographic Applications	Ç. K. Koç
01:30 - 03:00 pm	Introduction to Side-Channel Analysis	M. Joye
03:30 - 05:00 pm	Electromagnetic Analysis and Advance Side-Channel Analysis	P. Rohatgi

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## THURSDAY, June 22

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08:30 - 10:00 am	RSA - Side Channel Attacks & Countermeasures	M. Joye
10:30 - 12:00 pm	ECC - Side Channel Attacks & Countermeasures	M. Joye
01:30 - 03:00 pm	Side Channel Attacks to Block Ciphers: DES & AES	F.-X. Standaert
03:30 - 05:00 pm	Countermeasures for Block Ciphers	F.-X. Standaert

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## FRIDAY, June 23

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08:30 - 10:00 am	Random Number Generators for Cryptographic Applications	W. Schindler
10:30 - 12:00 pm	Evaluation Criteria Non-Deterministic Random Number Generators	W. Schindler
01:30 - 03:00 pm	Random Number Generator Design Constraints and Challenges	V. Fischer

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