



Delta-Sigma Data Converters
EPFL, LAUSANNE, SWITZERLAND
JUNE 29 – JULY 3, 2020

MONDAY, June 29

8:30-12:00 pm	Delta Sigma Converter Basics – Parts A & B	Shanthi Pavan
1:30-5:00 pm	Delta Sigma Converter Basics – Parts C & D	Shanthi Pavan

TUESDAY, June 30

8:30-10:00 am	Delta Sigma Converter Basics – Part E	Shanthi Pavan
10:30-12:00 pm	High-Level Design of CT-Delta Sigma Modulators	Shanthi Pavan
1:30-5:00 pm	Non-Idealities in Continuous-Time DS Modulators	Shanthi Pavan

WEDNESDAY, July 1

8:30-10:00 am	Design of Building Blocks for CTDS Modulators	Shanthi Pavan
10:30-12:00 pm	Systematic Design Centering of a Practical CTDSM	Shanthi Pavan
1:30-3:00 pm	Circuit Techniques to Mitigate Flicker Noise in CTDS Modulators	Shanthi Pavan
3:30-5:00 pm	Dynamic Element Matching (Part 1)	Ian Galton

THURSDAY, July 2

8:30-10:00 am	Dynamic Element Matching (Part 2)	Ian Galton
10:30-12:00 pm	VCO-Based Delta Sigma ADCs	Ian Galton
1:30-3:00 pm	Discrete-Time Delta Sigma Design	David Johns
3:30-5:00 pm	Introduction to the Delta Sigma Toolbox	David Johns

FRIDAY, July 3

8:30-10:00 am	Bandpass Delta Sigma ADCs	David Johns
10:30-12:00 pm	Incremental and Sensor ADCs	David Johns
1:30-3:00 pm	Circuit Noise Issues with ADCs	David Johns
