



Techniques for Handling Noise and Variability in Analog Circuits

EPFL PREMISES, LAUSANNE, SWITZERLAND
JANUARY 23-27, 2017

MONDAY, January 23

8:30-12:00 am	Random Mismatch Origins	Marcel Pelgrom
1:30-5:00 pm	Fundamentals of Noise in Electronic Devices	Christian Enz

TUESDAY, January 24

8:30-12:00 am	Analyzing Mismatch and Yield in Analog Circuits	Marcel Pelgrom
1:30-5:00 pm	Noise Analysis in Continuous-Time and Sampled-Data Circuits	Christian Enz

WEDNESDAY, January 25

8:30-10:00 am	Noise Cancellation Techniques	Willy Sansen
10:30-12:00 am	Noise Sampling in Switched Capacitor Filters	Willy Sansen
1:30-3:00 pm	Offset, CMRR and PSRR	Willy Sansen
3:30-5:00 pm	Variability in Bandgaps	Willy Sansen

THURSDAY, January 26

8:30-12:00 am	Layout Strategies to Reduce Offset	Marcel Pelgrom
1:30-5:00 pm	Dynamic Offset-Cancellation Techniques	Kofi Makinwa

FRIDAY, January 27

8:30-10:00 am	Dynamic Element Matching Techniques	Kofi Makinwa
10:30-12:00 am & 1:30-3:00 pm	gm/Id Methodology for Autozero Calibration of Analog Circuits and Systems	Maher Kayal
