



PLLs & Oscillators

EPFL, LAUSANNE, SWITZERLAND
JUNE 26-30, 2017

MONDAY, June 26

8:30-10:00 am	Fundamentals of Analog PLLs	Michiel Steyaert
10:30-12:00 am	Interference Effects in PLLs	Michiel Steyaert
1:30 -5:00 pm	Spiral Inductor Interference, Deadzone and Phase Noise	Michiel Steyaert

TUESDAY, June 27

8:30-12:00 am	VCO Design	Ali Hajimiri
1:30 -5:00 pm	Jitter and Phase Noise in PLLs	Ali Hajimiri

WEDNESDAY, June 28

8:30-12:00 am	All-Digital PLL Architecture and Implementation	Bogdan Staszewski
1:30-3:00 pm	Digitally Controlled Oscillator (DCO)	Bogdan Staszewski
3:30-5:00 pm	Time-to-Digital Converter (TDC)	Bogdan Staszewski

THURSDAY, June 29

8:30-10:00 am	Oscillators Basics: Feedback and Power Consumption	Willy Sansen
10:30-12:00 am	RC-Oscillators	Willy Sansen
1:30-3:00 pm	Designing XTAL and MEMS Oscillators from MHz to GHz	David Ruffieux
3:30-5:00 pm	Low Phase Noise and Low Jitter 0.1-10GHz VCO	David Ruffieux

FRIDAY, June 30

8:30-12:00 am	Fractional N-PLLs for Frequency Synthesis	Ian Galton
1:30-3:00 pm	FDC-Based Digital PLLs	Ian Galton
