



## Power Management

EPFL, LAUSANNE, SWITZERLAND  
AUGUST 26-30, 2019

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### MONDAY, August 26

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8:30-12:00 am & 1:30-3:00 pm	DC-DC Converters, Topologies & Control Techniques	Richard Redl
3:30-5:00 pm	Converter Modeling and Feedback Loop Design	Richard Redl

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### TUESDAY, August 27

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8:30-9:15 am	Power Stages and Gate Drivers I	Bernhard Wicht
9:15-10:00 am	Controller Chip Implementations for Switching Converters	Eduard Alarcon
10:30-12:00 pm	Adaptive Power Management for RF	Eduard Alarcon
1:30-3:00 pm	Fundamentals of Linear Regulators	Pavan Hanumolu
3:30-5:00 pm	LED Drivers Design	Pavan Hanumolu

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### WEDNESDAY, August 28

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8:30-9:15 am	Power Stages and Gate Drivers II	Bernhard Wicht
9:15-10:00 am	GaN Drivers	Bernhard Wicht
10:30-12:00 pm	Digitally Controlled DC-DC Converters	Pavan Hanumolu
1:30-3:00 pm	Time-Based Control of DC-DC Converters	Pavan Hanumolu
3:30-5:00 pm	Bandgap Voltage References	Willy Sansen

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### THURSDAY, August 29

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8:30-12:00 am	DC-DC: From Discrete To Fully CMOS Integrated	Michiel Steyaert
1:30-5:00 pm	Battery Charging Techniques & Circuits for Notebook Computers & Cellular Phones	Thomas Szepesi

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### FRIDAY, August 30

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8:30-10:00 am	Design of LDO's with instant Load Regulation and Unconditional Stability	Vadim Ivanov
10:30-12:00 am	Circuit Techniques for Integrated Switching Regulators	Vadim Ivanov
1:30-3:00 pm	Nanopower Design Techniques and Efficient Energy Harvesting	Vadim Ivanov

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