



Power Management

EPFL, LAUSANNE, SWITZERLAND
AUGUST 31 - SEPTEMBER 4, 2020

MONDAY, August 31

8:30-12:00 pm & 1:30-3:00 pm	DC-DC Converters, Topologies & Control Techniques	Richard Redl
3:30-5:00 pm	Converter Modeling and Feedback Loop Design	Richard Redl

TUESDAY, September 1

8:30-10:00 am	Power Stages	Bernhard Wicht
10:30-12:00 pm	Gate Drivers and Protection	Bernhard Wicht
1:30-3:00 pm	GaN Drivers and Circuit Design	Bernhard Wicht
3:30-5:00 pm	Charge Pumps and Switched-Capacitor Converters	Bernhard Wicht

WEDNESDAY, September 2

8:30-10:00 am	Hybrid Converters	Bernhard Wicht
10:30-12:00 pm	Bandgap Voltage References	Willy Sansen
1:30-3:00 pm	Fundamentals of Linear Regulators	Pavan Hanumolu
3:30-5:00 pm	LED Drivers Design	Pavan Hanumolu

THURSDAY, September 3

8:30-10:00 am	Digitally Controlled DC-DC Converters	Pavan Hanumolu
10:30-12:00 pm	Time-Based Control of DC-DC Converters	Pavan Hanumolu
1:30-5:00 pm	DC-DC: From Discrete To Fully CMOS Integrated	Michiel Steyaert

FRIDAY, September 4

8:30-10:00 am	Design of LDO's with Instant Load Regulation and Unconditional Stability	Vadim Ivanov
10:30-12:00 pm	Circuit Techniques for Integrated Switching Regulators	Vadim Ivanov
1:30-3:00 pm	Nanopower Design Techniques and Efficient Energy Harvesting	Vadim Ivanov
