



## Advanced Analog Circuit Design

UNIVERSITY OF SANTA CRUZ, CALIFORNIA  
MARCH 26-30, 2018

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### MONDAY, March 26

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8:30-10:00 am	Opamp Stability and Optimization	David Johns
10:30-12:00 am & 1:30-3:00 pm	Low Power OpAmp Design and Biasing	David Johns
3:30-5:00 pm	Circuit Noise limitations	David Johns

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### TUESDAY, March 27

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8:30-12:00 am	Time Assisted Analog Design	Pavan Hanumolu
1:30-5:00 pm	Circuit Techniques for OpAmp Speed and Accuracy Improvement	Vadim Ivanov

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### WEDNESDAY, March 28

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08:30-10:00 am	Voltage References	Vadim Ivanov
10:30-12:00 am & 1:30-3:00 pm	Continuous-Time Filters	Boris Murmann
3:30-5:00 pm	CMOS Switched-Capacitor Circuit Design	Boris Murmann

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### THURSDAY, March 29

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8:30-10:00 am	Offset and 1/f Noise Reduction Techniques	Boris Murmann
10:30-12:00 am & 1:30-3:00 pm	$G_m/I_D$ -based Design of Amplifier Circuits	Boris Murmann
3:30-5:00 pm	Digitally Enhanced Analog Design	Ian Galton

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### FRIDAY, March 30

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8:30-12:00 am	Digitally Enhanced Analog Design	Ian Galton
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