

Advanced Analog Circuit Design

UNIVERSITY OF SANTA CRUZ, CALIFORNIA

March 25-29, 2019

MONDAY, March 25

8:30-10:00 am	Opamp Stability and Optimization	David Johns
10:30-12:00 am & 1:30-3:00 pm	Low Power OpAmp Design and Biasing	David Johns
3:30-5:00 pm	Circuit Noise Limitations	David Johns

TUESDAY, March 26

08:30-12:00 am	Continuous-Time Filters	Boris Murmann
1:30-3:00 pm	CMOS Switched-Capacitor Circuit Design	Boris Murmann
3:30-5:00 pm	Offset and 1/f Noise Reduction Techniques	Boris Murmann

WEDNESDAY, March 27

8:30-12:00 am	G_m/I_D -based Design of Amplifier Circuits	Boris Murmann
1:30-5:00 pm	Time Assisted Analog Design	Pavan Hanumolu

THURSDAY, March 28

8:30-12:00 am & 1:30-3:00 pm	Digitally Enhanced Analog Design	Ian Galton
3:30-5:00 pm	Voltage References	Vadim Ivanov

FRIDAY, March 29

8:30-12:00 am	Circuit Techniques for OpAmp Speed and Accuracy Improvement	Vadim Ivanov
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