



## Advanced Analog & Mixed-Signal Design

UNIVERSITY OF SANTA CRUZ, CALIFORNIA  
MARCH 27-31, 2017

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### MONDAY, March 27

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8:30 - 10:00 am	Noise Coupling in Mixed-Mode ICs: Mechanisms	Tim Schmerbeck
10:30 - 12:00 am	Noise Coupling in Mixed-Mode ICs: Simulation/ Measurement	
2:00 - 3:30 pm	Noise Coupling in Mixed-Mode ICs: Design Strategy/Hardware Example	Tim Schmerbeck
3:45 - 5:15 pm	ESD & EMC Robustness in Silicon ICs	Tim Schmerbeck

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### TUESDAY, March 28

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8:30 - 10:00 am	ESD & EMC Robustness in Silicon ICs (Continued)	Tim Schmerbeck
10:30 - 12:00 am & 2:00 - 5:15 pm	Digitally Enhanced Analog Design	Ian Galton

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### WEDNESDAY, March 29

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08:30 - 12:00 am	Time Assisted Analog Design	Pavan Hanumolu
2:00 - 5:15 pm	Continuous-Time Filters	Boris Murmann

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### THURSDAY, March 30

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8:30 - 10:00 am	CMOS Switched-Capacitor Circuit Design	Boris Murmann
10:30 - 12:00 am	Offset and 1/f Noise Reduction Techniques	Boris Murmann
2:00 - 5:15 pm	$G_m/I_D$ -based Design of Amplifier Circuits	Boris Murmann

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### FRIDAY, March 31

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8:30 - 12:00 am	Circuit Techniques for OpAmp Speed and Accuracy Improvement	Vadim Ivanov
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