



# Advanced Analog CMOS IC Design

EPFL PREMISES, LAUSANNE, SWITZERLAND  
AUGUST 23-27, 2010

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## MONDAY, AUGUST 23

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8:30 - 12:00 am	MOS and Bipolar: Modes of Operation and Models	Eric Vittoz
1:30 - 5:00 pm	Passive Components and Parasitic Effects	Eric Vittoz

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## TUESDAY, AUGUST 24

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8:30 - 12:00 am	Elementary Building Blocks	Eric Vittoz
1:30 - 5:00 pm	CMOS Off-Chip Drivers	Rinaldo Castello

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## WEDNESDAY, AUGUST 25

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8:30 - 12:00 am & 1:30 - 5:00 pm	Analog Functional Blocks	Willy Sansen
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## THURSDAY, AUGUST 26

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8:30 - 10:00 am	Layout Techniques for Analog Circuits	Eric Vittoz
10:30 - 12:00 am	Voltage References	Eric Vittoz
1:30 - 5:00 pm	Technology and other Limitations for Analog Design	Herman Casier

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## FRIDAY, AUGUST 27

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8:30 - 10:00 am	Technology and other Limitations for Analog Design	Herman Casier
10:30 - 12:00 am & 1:30 - 5:00 pm	Switched-Capacitor Circuit Design	Gabor Temes

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