



MEAD Education S.A.

Advanced Analog CMOS IC Design

EPFL, LAUSANNE, SWITZERLAND

AUGUST 25-29, 2014

MONDAY, August 25

8:30-12:00 am	MOS and Bipolar: Modes of Operation and Models	Eric Vittoz
1:30-3:00 pm	Passive Components and Parasitic Effects	Eric Vittoz
3:30 -5:00 pm	Basic Layout Techniques	Eric Vittoz

TUESDAY, August 26

8:30-12:00 am	Elementary Building Blocks	Eric Vittoz
1:30-3:00 pm	Voltage References	Eric Vittoz
3:30-5:00 pm	Technology and other Limitations for Analog Design	Herman Casier

WEDNESDAY, August 27

8:30-12:00 am	Technology and other Limitations for Analog Design	Herman Casier
1:30-5:00 pm	Analog Functional Blocks	Willy Sansen

THURSDAY, August 28

8:30-12:00 am	Analog Functional Blocks	Willy Sansen
1:30-5:00 pm	CMOS Off-Chip Drivers	Rinaldo Castello

FRIDAY, August 29

8:30-12:00 am & 1:30 - 3:00 pm	CMOS Switched-Capacitor Circuit Design	Gabor Temes
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