

Cryptographic Engineering

EPFL, LAUSANNE, SWITZERLAND JUNE 29 – JULY 3, 2015

MONDAY, June 29		
08:30 - 10:00 am	Introduction to Block Ciphers; DES and AES	C. Paar
10:30 - 12:00 pm	Lightweight Block Ciphers for RFIDs	C. Paar
01:30 - 03:00 pm	Modular Arithmetic Algorithms and Architectures	C. K. Koc
03:30 - 05:00 pm	Finite Fields Algorithms and Architectures	C. K. Koc
TUESDAY, Ju	une 30	
08:30 - 10:00 am	Specialized Hardware for Secret-Key Algorithms	I. Verbauwhede
10:30 - 12:00 pm	Introduction to PUFs (Physically Unclonable Functions)	I. Verbauwhede
01:30 - 03:00 pm	Public-Key Cryptography: Algorithms and Protocols	C. K. Koc
03:30 - 05:00 pm	Finite Fields and Groups: Algorithms and Architectures	C. K. Koc
WEDNESDAY	/, July 1	
08:30 - 10:00 am	Trusted Computing Architectures, SSL and IPSec	P. Rohatgi
10:30 - 12:00 pm	Fast and Verified Curve25519 Software	P. Schwabe
01:30 - 03:00 pm	Introduction to Side-Channel Analysis	M. Joye
03:30 - 05:00 pm	Electromagnetic Analysis and Advance Side-Channel Analysis	P. Rohatgi
THURSDAY,	July 2	
08:30 - 10:00 am	RSA - Side Channel Attacks & Countermeasures	M. Joye
10:30 - 12:00 pm	ECC - Side Channel Attacks & Countermeasures	M. Joye
01:30 - 03:00 pm	Side Channel Attacks to Block Ciphers: DES & AES	FX. Standaert
03:30 - 05:00 pm	Countermeasures for Block Ciphers	FX. Standaert
FRIDAY, July	' 3	
08:30 - 10:00 am	Random Number Generators for Cryptographic Applications	W. Schindler
10:30 - 12:00 pm	Evaluation Criteria Non-Deterministic Random Number Generators	W. Schindler
01:30 - 03:00 pm	Random Number Generator Design Constraints and Challenges	V. Fischer