



HEAD *Microelectronics, Inc.*

STATE-OF-THE-ART DATA CONVERTER DESIGN

UC SANTA CRUZ, CALIFORNIA

MARCH 25-29, 2013

MONDAY, March 25

8:30 - 10:00 am	Delta-Sigma ADCs: Introduction	Gabor Temes
10:30 - 12:00 am	Incremental and Extended-Counting ADCs	Gabor Temes
2:00 - 3:30 pm	Higher-Order Delta-Sigma ADCs	Richard Schreier
3:45 - 5:15 pm	High-Level Simulation of Delta-Sigma ADCs	Richard Schreier

TUESDAY, March 26

8:30 - 10:00 am	Design Examples of Delta-Sigma ADCs	Richard Schreier
10:30 - 12:00 am	Bandpass Delta-Sigma ADCs	Richard Schreier
2:00 - 3:30 pm	Continuous-Time Delta-Sigma ADCs	Richard Schreier
3:45 - 5:15 pm	Delta-Sigma DACs	Gabor Temes

WEDNESDAY, March 27

8:30 - 12:00 am	Fundamentals of VCO-Based ADCs	Pavan K. Hanumolu
2:00 - 5:15 pm	State-of-the-Art VCO-Based ADCs	Pavan K. Hanumolu

THURSDAY, March 28

8:30 - 10:00 am	SAR ADCs	Gabor Temes
13:30 - 12:00 am	Mismatch-Shaping Multi-bit DACs	Ian Galton
2:00 - 3:30 pm	Pipeline ADCs	Ian Galton
3:45 - 5:15 pm	Mostly-Digital Reconfigurable Oversampling VCO-based ADCs	Ian Galton

FRIDAY, March 29

8:30 - 10:00 am	Nyquist-Rate Current-Steering DACs	Ian Galton
10:30 - 12:00 am & 1:00 - 2:30 pm	Flash ADCs	Maarten Vertregt
