



MEAD Education S.A.

Continuous-Time Delta-Sigma Design

EPFL, LAUSANNE, SWITZERLAND
JUNE 30 – JULY 3, 2014

MONDAY, June 30

8:30 - 12:00 am	Delta Sigma Converter Basics, Parts1 & 2	Shanthi Pavan
1:30 - 3:00 am	High Level Design of CT-Delta Sigma Modulators	Shanthi Pavan
3:30 - 5:00 pm	Non-idealities in CTDSMs, Part 1	Shanthi Pavan

TUESDAY, July 1

8:30 - 10:00 am	Non-idealities in CTDSMs, Part 2	Shanthi Pavan
10:30 - 12:00 am	Implementation: ADCs and Feedback DACs	Shanthi Pavan
1:30 - 3:00 pm	Alias Rejection, Noise and Circuit Simulation	Shanthi Pavan
3:30 - 5:00 pm	Mismatch Correction	Shanthi Pavan

WEDNESDAY, July 2

8:30 - 10:00 am	Dynamic Element Matching, Part 1	Shanthi Pavan
10:30 - 12:00 am	Dynamic Element Matching, Part 2 and Lab Bench testing	Shanthi Pavan
1:30 - 3:00 pm	Introduction to the DS Toolbox	Richard Schreier
3:00 - 5:00 pm	Continuous-Time Design with the DS Toolbox	Richard Schreier

THURSDAY, July 3

8:30 - 10:00 am	Continuous-Time Bandpass ADCs	Richard Schreier
10:30 - 12:00 am	Continuous-Time Quadrature (and Polyphase) ADCs	Richard Schreier
1:30 - 3:00 pm	High-speed DACs for Continuous-Time ADCs	Richard Schreier
