



## Continuous-Time Delta-Sigma Design

**EPFL, LAUSANNE, SWITZERLAND  
JUNE 26-30, 2017**

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### **MONDAY, JUNE 26**

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8:30 - 12:00 am	Delta Sigma Converter Basics – Parts A & B	Shanthi Pavan
1:30 - 5:00 pm	Delta Sigma Converter Basics – Parts C & D	Shanthi Pavan

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### **TUESDAY, JUNE 27**

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8:30 - 10:00 am	Delta Sigma Converter Basics – Part E	Shanthi Pavan
10:30 - 12:00 am	Introduction to the DS Toolbox	Richard Schreier
1:30 - 5:00 pm	High-Level Design of CT-Delta Sigma Modulators	Shanthi Pavan

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### **WEDNESDAY, JUNE 28**

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8:30 - 10:00 am	Continuous-Time Design with the DS Toolbox	Richard Schreier
10:30 - 12:00 am	Non-Idealities in Continuous-Time DS Modulators	Shanthi Pavan
1:30 - 3:00 pm	Design of building blocks for CTDS Modulators	Shanthi Pavan
3:30 - 5:00 pm	Systematic Design of a Practical CTDS Modulators	Shanthi Pavan

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### **THURSDAY, JUNE 29**

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8:30 - 10:00 am	Dynamic Element Matching (Part 1)	Shanthi Pavan
10:30 - 12:00 am	Dynamic Element Matching (Part 2)	Ian Galton
1:30 - 5:00 pm	VCO-Based Delta Sigma ADCs	Ian Galton

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### **FRIDAY, JUNE 30**

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8:30 - 10:00 am	Continuous-Time Bandpass ADCs	Richard Schreier
10:30 - 12:00 am	Quadrature and Polyphase CTDSMs	Richard Schreier
1:30 - 3:00 pm	High-speed DACs for Continuous-Time ADCs	Richard Schreier

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