



**Delta-Sigma Data Converters**  
**EPFL, LAUSANNE, SWITZERLAND**  
**JUNE 18-22, 2018**

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**MONDAY, JUNE 18**

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8:30 - 12:00 am	Delta Sigma Converter Basics – Parts A & B	Shanthi Pavan
1:30 - 5:00 pm	Delta Sigma Converter Basics – Parts C & D	Shanthi Pavan

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**TUESDAY, JUNE 19**

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8:30 - 10:00 am	Delta Sigma Converter Basics – Part E	Shanthi Pavan
10:30 - 12:00 am	High-Level Design of CT-Delta Sigma Modulators	Shanthi Pavan
1:30 - 5:00 pm	Non-Idealities in Continuous-Time DS Modulators	Shanthi Pavan

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**WEDNESDAY, JUNE 20**

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8:30 - 10:00 am	Discrete-Time Delta Sigma Design	David Johns
10:30 - 12:00 am	Introduction to the Delta Sigma Toolbox	David Johns
1:30 - 3:00 pm	Bandpass Delta Sigma ADCs	David Johns
3:30 - 5:00 pm	Incremental and Sensor ADCs	David Johns

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**THURSDAY, JUNE 21**

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8:30 - 10:00 am	Circuit Noise Issues with ADCs	David Johns
10:30 - 12:00 am	Design of Building Blocks for CTDS Modulators	Shanthi Pavan
1:30 – 3:00 pm	Systematic Design Centering of a Practical CTDSM	Shanthi Pavan
3:30 - 5:00 pm	Circuit Techniques to Mitigate Flicker Noise in CTDS Modulators	Shanthi Pavan

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**FRIDAY, JUNE 22**

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8:30 - 10:00 am	Dynamic Element Matching (Part 1)	Ian Galton
10:30 - 12:00 am	Dynamic Element Matching (Part 2)	Ian Galton
1:30 - 3:00 pm	VCO-Based Delta Sigma ADCs	Ian Galton

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