



MEAD *Microelectronics, Inc.*

Continuous-Time Delta-Sigma Design

UNIVERSITY OF SANTA CRUZ, CALIFORNIA

MARCH 23-27, 2015

MONDAY, March 23

8:30 - 12:00 am	Delta Sigma Converter Basics – 1&2	Shanthi Pavan
2:00 - 5:15 pm	Delta Sigma Converter Basics – 3&4	Shanthi Pavan

TUESDAY, March 24

8:30 - 10:00 am	Introduction to the DS Toolbox	Richard Schreier
10:30 - 12:00 am	Continuous-Time Design with the DS Toolbox	Richard Schreier
2:00 - 3:30 pm	Continuous-Time Bandpass ADCs	Richard Schreier
3:45 - 5:15 pm	High-speed DACs for Continuous-Time ADCs	Richard Schreier

WEDNESDAY, March 25

8:30 - 12:00 am	High-Level Design of CT Delta-Sigma Modulators	Shanthi Pavan
2:00 - 5:15pm	Non-Idealities in CTDSMs - 1&2	Shanthi Pavan

THURSDAY, March 26

8:30 - 10:00 am	Design of Building Blocks for CTDSMs	Shanthi Pavan
10:30 - 12:00 am	Systematic Design of a Practical CTDSM	Shanthi Pavan
2:00 - 3:30 pm	Dynamic Element Matching	Shanthi Pavan
3.45 - 5:15pm	VCO-Based Delta-Sigma ADCs	Pavan Hanumolu

FRIDAY, March 27

8:30 - 12:00 am	VCO-Based Delta-Sigma ADCs	Pavan Hanumolu
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