



MEAD Education S.A.

High-Performance Data Converters

EPFL, LAUSANNE, SWITZERLAND

JUNE 20-24, 2016

Preliminary Program

MONDAY, June 20

8:30-10:00 am	Overview of High-Speed Data Converters	Marcel Pelgrom
10:30-12:00 am	Fundamental Limitations	Marcel Pelgrom
1:30-5:00 pm	Flash ADCs	Marcel Pelgrom

TUESDAY, June 21

8:30-10:00 am	Interleaved ADCs	Marcel Pelgrom
10:30-12:00 am	Fundamentals of SAR ADCs	Boris Murmann
1:30-3:00 pm	Recent Developments in SAR ADCs	Boris Murmann
3:30-5:00 pm	Introduction to Pipelined ADCs	Ian Galton

WEDNESDAY, June 22

8:30-10:00 am	Sample-and-Hold and Comparator Design	Boris Murmann
10:30-12:00 am	OpAmp Design for ADCs	Boris Murmann
1:30-5:00 pm	Mismatch-Shaping Multi-bit DACs	Ian Galton

THURSDAY, June 23

8:30-10:00 am	Pipeline ADCs with Digital Calibration	Ian Galton
10:30-12:00 am	Nyquist- Rate Current-Steering DACs	Ian Galton
1:30-3:00 pm	High-Speed DACs	Klaas Bult
3:30-5:00 pm	VCO-based ADC Techniques	Ian Galton

FRIDAY, June 24

8:30-10:00 am	High-Speed DACs (cont'd)	Klaas Bult
10:30-12:00 am & 1:30-3:00 pm	Embedded ADCs	Klaas Bult
