



High-Performance Data Converters

EPFL, LAUSANNE, SWITZERLAND
JUNE 26-30, 2017

MONDAY, June 26

8:30-10:00 am	Overview of High-Speed Data Converters	Marcel Pelgrom
10:30-12:00 am	Fundamental Limitations	Marcel Pelgrom
1:30-5:00 pm	Flash ADCs	Marcel Pelgrom

TUESDAY, June 27

8:30-10:00 am	Interleaved ADCs	Marcel Pelgrom
10:30-12:00 am & 1:30-3:00 pm	Mismatch-Shaping Multi-bit DACs for Delta-Sigma Data Converters	Ian Galton
3:30-5:00 pm	SAR ADCs	Klaas Bult

WEDNESDAY, June 28

8:30-10:00 am	Introduction to Pipelined ADCs	Ian Galton
10:30-12:00 am	Pipeline ADCs with Digital Calibration	Ian Galton
1:30-3:00 pm	VCO-based ADC Techniques	Ian Galton
3:30-5:00 pm	ADC Building Blocks	Klaas Bult

THURSDAY, June 29

8:30-10:00 am	DEM for Nyquist-Rate Current-Steering DACs	Ian Galton
10:30-12:00 am	Comparison of ADC Architectures	Klaas Bult
1:30-5:00 pm	Power Dissipation in ADCs	Klaas Bult

FRIDAY, June 30

8:30-12:00 am	Embedded ADCs	Klaas Bult
1:30-3:00 pm	High-Speed DACs	Klaas Bult
