



## High-Performance Data Converters

EPFL, LAUSANNE, SWITZERLAND  
JUNE 25-29, 2018

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### MONDAY, June 25

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8:30-10:00 am	Overview of High-Speed Data Converters	Marcel Pelgrom
10:30-12:00 am	Fundamental Limitations	Marcel Pelgrom
1:30-5:00 pm	Flash ADCs	Marcel Pelgrom

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### TUESDAY, June 26

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8:30-10:00 am	Interleaved ADCs	Marcel Pelgrom
10:30-12:00 am & 1:30-3:00 pm	Mismatch-Shaping Multi-bit DACs for Delta-Sigma Data Converters	Ian Galton
3:30-5:00 pm	SAR ADCs	Klaas Bult

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### WEDNESDAY, June 27

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8:30-10:00 am	Comparison of ADC Architectures	Klaas Bult
10:30-12:00 am	ADC Building Blocks	Klaas Bult
1:30-3:00 pm	Introduction to Pipeline ADCs	Ian Galton
3:30-5:00 pm	Pipeline ADCs with Digital Calibration	Ian Galton

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### THURSDAY, June 28

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8:30-12:00 am	Power Dissipation in ADCs	Klaas Bult
1:30-3:00 pm	VCO-based ADC Techniques	Ian Galton
3:30-5:00 pm	DEM for Nyquist-Rate Current-Steering DACs	Ian Galton

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### FRIDAY, June 29

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8:30-12:00 am	High-Speed DACs	Klaas Bult
1:30-3:00 pm	Embedded ADCs	Klaas Bult

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