



**MEAD** Education S.A.

## **HV and Automotive Circuit Design**

**EPFL, LAUSANNE, SWITZERLAND**

**August 25-27, 2015**

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### **TUESDAY, August 25**

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8:30-10:00 am	Introduction : HV/High Temperature Challenges	Herman Casier
10:30-12:00 am	High Voltage/High Temperature Limitations	Herman Casier
1:30-3:00 pm	Automotive sensors	Dirk Hammerschmidt
3:30-5:00 pm	Functional safety according to ISO26262 in IC development	Dirk Hammerschmidt

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### **WEDNESDAY, August 26**

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8:30-12:00 am	Power Switchers and Gate Drivers	Bernhard Wicht
1:30-3:00 pm	(Automotive) Power Management	Bernhard Wicht
3:30-5:00 pm	Environment Interferences	Herman Casier

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### **THURSDAY, August 27**

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8:30-12:00 am	Circuit Design for EMC	Michiel Steyaert
1:30-5:00 pm	ESD protection in HV Design	Tim Schmerbeck

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