



## HV and Automotive Circuit Design

EPFL, LAUSANNE, SWITZERLAND  
AUGUST 29 – SEPTEMBER 1, 2017

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### TUESDAY, August 29

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8:30-10:00 am	High-Voltage/High-Temperature Challenges	Herman Casier
10:30-12:00 am	High-Voltage/High-Temperature Limitations	Herman Casier
1:30-5:00 pm	ESD protection in High-Voltage Design	Tim Schmerbeck

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### WEDNESDAY, August 30

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8:30-10:00 am	High-Voltage/High-Temperature Limitations	Herman Casier
10:30-12:00 am	Environment Interferences	Herman Casier
1:30-3:00 pm	Power Switches	Bernhard Wicht
3:30-5:00 pm	Gate Drivers and Protection	Bernhard Wicht

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### THURSDAY, August 31

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8:30-10:00 am	Linear Regulators and Charge Pumps	Bernhard Wicht
10:30-12:00 am	Switched Mode Power Supplies	Bernhard Wicht
1:30-5:00 pm	Circuit Design for EMC	Michiel Steyaert

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### FRIDAY, September 1

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8:30-10:00 am	Functional Safety According to ISO26262 in IC Development	Dirk Hammerschmidt
10:30-12:00 am & 1:30-3:00 pm	Automotive Sensors	Dirk Hammerschmidt

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