



MEAD Education S.A.

LOW-POWER ANALOG IC DESIGN

EPFL, LAUSANNE, SWITZERLAND

JULY 1-5, 2013

MONDAY, July 1

| | | |
|-----------------|--|---------------|
| 8:30 - 12:00 am | MOS Transistor Modeling for Low-Voltage and Low-Power Circuit Design | Christian Enz |
| 1:30 - 3:00 pm | Amplifiers, Source Followers & Cascodes | Willy Sansen |
| 3:30 - 5:00 pm | Differential Voltage & Current Amplifiers | Willy Sansen |

TUESDAY, July 2

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|------------------|---|--------------|
| 8:30 - 10:00 am | Stability of Operational Amplifiers | Willy Sansen |
| 10:30 - 12:00 am | Systematic Design of Low-Power Operational Amplifiers | Willy Sansen |
| 1:30 - 3:00 pm | Important Opamp Configurations | Willy Sansen |
| 3:30 - 5:00 pm | Noise Performance of Elementary Transistor Stages | Willy Sansen |

WEDNESDAY, July 3

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|------------------|--|--------------|
| 8:30 - 10:00 am | Fully-Differential Operational Amplifiers | Willy Sansen |
| 10:30 - 12:00 am | Bandgap and Current Reference Circuits | Willy Sansen |
| 1:30 - 3:00 pm | Distortion in Elementary Transistor Circuits | Willy Sansen |
| 3:30 - 5:00 pm | Low-Power Continuous-Time Filters | Willy Sansen |

THURSDAY, July 4

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|-----------------|---|---------------|
| 8:30 - 12:00 am | Ultra-Low Voltage Analog Circuit Design | Christian Enz |
| 1:30 - 5:00 pm | Micropower ADCs | Kofi Makinwa |

FRIDAY, July 5

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|-----------------|--|----------------|
| 8:30 - 12:00 am | Matching of MOS Transistors in Deep-Submicron | Marcel Pelgrom |
| 1:30 - 3:00 pm | Layout Considerations in Mixed-Signal Circuit Design | Marcel Pelgrom |
