



Low-Power Analog IC Design

EPFL, LAUSANNE, SWITZERLAND
JUNE 24-28, 2019

MONDAY, JUNE 24

8:30 - 12:00 am	MOS Transistor Modeling for Low-Voltage and Low-Power Circuit Design	Christian Enz
1:30 - 3:00 pm	Low-Power Low-Voltage Biasing for Single-Transistor Stages	Willy Sansen
3:30 - 5:00 pm	Differential Amplifying Blocks with Positive Feedback	Willy Sansen

TUESDAY, JUNE 25

8:30 - 10:00 am	Noise Performance of Elementary Transistor Stages	Willy Sansen
10:30 - 12:00 am	Stability of Operational Amplifiers	Willy Sansen
1:30 - 3:00 pm	Systematic Design of Low-Power Operational Amplifiers	Willy Sansen
3:30 - 5:00 pm	Important Opamp Configurations	Willy Sansen

WEDNESDAY, JUNE 26

8:30 - 10:00 am	Fully-Differential Operational Amplifiers	Willy Sansen
10:30 - 12:00 am	Bandgap and Current Reference Circuits	Willy Sansen
1:30 - 5:00 pm	Micropower ADCs	Kofi Makinwa

THURSDAY, JUNE 27

8:30 - 10:00 am	Distortion in Elementary Transistor Circuits	Willy Sansen
10:30 - 12:00 am	Low-Power Continuous-Time Filters	Willy Sansen
1:30 - 5:00 pm	Matching of MOS Transistors in Deep-Submicron	Marcel Pelgrom

FRIDAY, JUNE 28

8:30 - 10:00 am	Layout Considerations in Mixed-Signal Circuit Design	Marcel Pelgrom
10:30 - 12:00 am & 1:30 - 3:00 pm	Ultra-Low Voltage Analog Circuit Design	Christian Enz
