



## Low-Power Analog IC Design

EPFL, LAUSANNE, SWITZERLAND  
JUNE 29-JULY 3, 2020

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### MONDAY, June 29

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8:30-12:00 pm	MOS Transistor Modeling for Low-Voltage and Low-Power Circuit Design	Christian Enz
1:30-3:00 pm	Basic low-Power low-Voltage Circuit Techniques	Willy Sansen
3:30-5:00 pm	Differential Amplifying Blocks with Positive Feedback	Willy Sansen

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### TUESDAY, June 30

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8:30-10:00 am	Noise Performance of Elementary Transistor Stages	Willy Sansen
10:30-12:00 pm	Stability of Operational Amplifiers	Willy Sansen
1:30-5:00 pm	Micropower ADCs	Kofi Makinwa

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### WEDNESDAY, July 1

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8:30-10:00 am	Systematic Design of Low-Power Operational Amplifiers	Willy Sansen
10:30-12:00 pm	Important Opamp Configurations	Willy Sansen
1:30-3:00 pm	Fully-Differential Operational Amplifiers	Willy Sansen
3:30-5:00 pm	Bandgap and Current Reference Circuits	Willy Sansen

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### THURSDAY, July 2

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8:30-10:00 am	Distortion in Elementary Transistor Circuits	Willy Sansen
10:30-12:00 pm	Low-Power Continuous-Time Filters	Willy Sansen
1:30-5:00 pm	Matching of MOS Transistors in Deep-Submicron	Marcel Pelgrom

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### FRIDAY, July 3

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8:30-10:00 am	Layout Considerations in Mixed-Signal Circuit Design	Marcel Pelgrom
10:30-12:00 pm & 1:30-3:00 pm	Ultra-Low Voltage Analog Circuit Design	Christian Enz

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