



**MEAD** *Microelectronics, Inc.*

## **LOW-POWER ANALOG IC DESIGN**

**UC SANTA CRUZ, CALIFORNIA**

**MARCH 25-29, 2013**

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### **MONDAY, March 25**

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8:30 - 10:00 am	MOS Transistor Modeling for Low-Voltage and Low-Current Circuit Design	Willy Sansen
10:30 - 12:00 am	Limits to Low-Voltage, Low-Power Analog Design	Willy Sansen
2:00 - 5:15 pm	Basic Low-Voltage, Low-Power Circuit Techniques	Willy Sansen

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### **TUESDAY, March 26**

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8:30 - 10:00 am	Stability of Operational Amplifiers	Willy Sansen
10:30 - 12:00 am	Systematic Design of Low-Power OpAmps	Willy Sansen
2:00 - 3:30 pm	Important OpAmp Configurations	Willy Sansen
3:45 - 5:15 pm	Noise Performance of Elementary Transistor Stages	Willy Sansen

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### **WEDNESDAY, March 27**

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8:30 - 10:00 am	Fully-Differential Operational Amplifiers	Willy Sansen
10:30 - 12:00 am	Bandgap and Current Reference Circuits	Willy Sansen
2:00 - 3:30 pm	Distortion in Elementary Transistor Circuits	Willy Sansen
3:45 - 5:15 pm	Low-Power Continuous-Time Filters	Willy Sansen

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### **THURSDAY, March 28**

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8:30 - 12:00 am	Matching of MOS Transistors in Deep-Submicron	Maarten Vertregt
2:00 - 5:15pm	Layout of Analog Circuits	Maarten Vertregt

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### **FRIDAY, March 29**

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8:30 - 12:00 pm	Micro-Power Data Converters	Gabor Temes
1:00 - 2:30 pm	Energy Harvesting	Ken Pedrotti

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