

Micro-Power Analog IC Design

**EPFL PREMISES, LAUSANNE, SWITZERLAND
JUNE 21-25, 2010**

MONDAY, JUNE 21

8:30 - 12:00 am	MOS Transistor Modeling for Low-Voltage and Low-Power Circuit Design	Christian Enz
1:30 - 5:00 pm	Basic Low-Power, Low-Voltage Circuit Techniques	Eric Vittoz

TUESDAY, JUNE 22

8:30 - 10:00 am	Stability of Operational Amplifiers	Willy Sansen
10:30 - 12:00 am	Systematic Design of Low-Power Operational Amplifiers	Willy Sansen
1:30 - 3:00 pm	Important Opamp Configurations	Willy Sansen
3:30 - 5:00 pm	Noise Performance of Elementary Transistor Stages	Willy Sansen

WEDNESDAY, JUNE 23

8:30 - 10:00 am	Fully-Differential Operational Amplifiers	Willy Sansen
10:30 - 12:00 am	Bandgap and Current Reference Circuits	Willy Sansen
1:30 - 3:00 pm	Distortion in Elementary Transistor Circuits	Willy Sansen
3:30 - 5:00 pm	Low-Power Continuous-Time Filters	Willy Sansen

THURSDAY, JUNE 24

8:30 - 12:00 am	Matching of MOS Transistors in Deep-Submicron	Marcel Pelgrom
1:30 - 5:00 pm	Layout Considerations in Mixed-Signal Circuit Design	Marcel Pelgrom

FRIDAY, JUNE 25

8:30 - 12:00 am	Ultra-Low Voltage Analog Circuit Design	Christian Enz
1:30 - 3:00 pm	Ultra-Low Power Opampless ADCs	Kush Gulati
