



## Micro-Power Analog IC Design

UC SANTA CRUZ, CALIFORNIA  
MARCH 22-26, 2010

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### MONDAY, MARCH 22

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8:30 - 10:00 am	MOS Transistor Modeling for Low-Voltage and Low-Current Circuit Design	Eric Vittoz
10:30 - 12:00 am	Limits to Low-Voltage, Low-Power Analog Design	Eric Vittoz
1:30 - 5:00 pm	Basic Low-Voltage, Low-Power Circuit Techniques	Eric Vittoz

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### TUESDAY, MARCH 23

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8:30 - 10:00 am	Stability of Operational Amplifiers	Willy Sansen
10:30 - 12:00 am	Systematic Design of Low-Power Operational Amplifiers	Willy Sansen
1:30 - 3:00 pm	Important Opamp Configurations	Willy Sansen
3:30 - 5:00 pm	Noise Performance of Elementary Transistor Stages	Willy Sansen

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### WEDNESDAY, MARCH 24

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8:30 - 10:00 am	Fully-Differential Operational Amplifiers	Willy Sansen
10:30 - 12:00 am	Bandgap and Current Reference Circuits	Willy Sansen
1:30 - 5:00 pm	Micro-Power Data Converters	Gabor Temes

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### THURSDAY, MARCH 25

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8:30 - 10:00 am	Distortion in Elementary Transistor Circuits	Willy Sansen
10:30 - 12:00 am	Low-Power Continuous-Time Filters	Willy Sansen
1:30 - 5:00 pm	Matching of MOS Transistors in Deep Submicron Technology	Maarten Vertregt

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### THURSDAY, MARCH 25

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8:30 - 12:00 am	Layout of Analog Circuits	Maarten Vertregt
1:00 - 2:30 pm	Low-Power Oscillator Design	Ken Pedrotti

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