



Migrating Analog Circuit Design to Scaled CMOS Technologies

EPFL, LAUSANNE, SWITZERLAND
JANUARY 21-24, 2019

MONDAY, January 21

8:30-12:00 am	Design Principles with MOST's at Low & High Frequencies	Willy Sansen
1:30-5:00 pm	Simple Modeling of MOS Transistors for Bulk, FDSOI and FinFET Technologies	Christian Enz

TUESDAY, January 22

8:30-12:00 am	Design Principles with MOST's at Low & High Frequencies	Willy Sansen
1:30-5:00 pm	Systematic Design of Analog Circuits using the Inversion Coefficient	Christian Enz

WEDNESDAY, January 23

8:30-12:00 am	Scaling Laws and Device Physics	Marcel Pelgrom
1:30-5:00 pm	Mismatch, offset and lay-out effects in scaled CMOS	Marcel Pelgrom

THURSDAY, January 24

8:30-12:00 am	Design and layout with FinFET's	Bertrand Parvais
1:30-5:00 pm	Design in FDSOI	Andreia Cathelin
