

Power Management

**EPFL PREMISES, LAUSANNE, SWITZERLAND
AUGUST 30 - SEPTEMBER 3, 2010**

MONDAY, AUGUST 30

8:30 - 12:00 am	DC-DC Converters, Topologies & Control Techniques	Richard Redl
1:30 - 3:00 pm	Converter Modeling and Feedback Loop Design	Richard Redl
3:30 - 5:00 pm	Fundamentals of Switched-Mode Power Supplies for Portable Applications	Eduard Alarcon

TUESDAY, AUGUST 31

8:30 - 10:00 am	Control Techniques and their Integrated Circuit Implementation for Switched-Mode Converters for Portable Applications	Eduard Alarcon
10:30 - 12:00 am	Adaptive Power Management Techniques for Portable Applications	Eduard Alarcon
1:30 - 5:00 pm	Bandgap References	Barrie Gilbert

WEDNESDAY, SEPTEMBER 1

8:30 - 12:00 am	CMOS Linear Regulators, Design and Case Studies	Maher Kayal
1:30 - 5:00 pm	Battery Charging Techniques & Circuits for Notebook Computers & Cellular Phones	Thomas Szepesi

THURSDAY, SEPTEMBER 2

8:30 - 10:00 am	Transistor-Level Off-Line DC-DC Controller Design	Robert Blauschild
10:30 - 12:00 am & 1:30 - 5:00 pm	Switched-Capacitor Power Supplies	Jesper Steensgaard

FRIDAY, SEPTEMBER 3

8:30 - 10:00 am	Power CMOS and BCD Linear Amplifier Design	Vadim Ivanov
10:30 - 12:00 am & 1:30 - 3:00 pm	Circuit Techniques for Integrated Switching Regulators	Vadim Ivanov Barrie Gilbert
