MEAD *Education S.A.*

POWER MANAGEMENT
EPFL, LAUSANNE, SWITZERLAND
AUGUST 26-30, 2013

MONDAY, August 26

8:30-12:00 am	DC-DC Converters, Topologies & Control Techniques	Richard Redl
1:30-3:00 pm	DC-DC Converters, Topologies & Control Techniques	Richard Redl
3:30-5:00 pm	Converter Modeling and Feedback Loop Design	Richard Redl

TUESDAY, August 27

8:30-10:00 am	Control Techniques and their IC Implementations for Switched-Mode Converters in Portable Applications	Eduard Alarcon
10:30-12:00 pm	Adaptive Power Management Techniques for Portable Applications	Eduard Alarcon
1:30-5:00 pm	Battery Charging Techniques & Circuits for Notebook Computers & Cellular Phones	Thomas Szepesi

WEDNESDAY, August 28

| 8:30-12:00 am | DC-DC: From Discrete To Fully CMOS Integrated | Michiel Steyaert |
| 1:30-5:00 pm | CMOS Linear Regulators & LP Photo Voltaic Harvester: Design and Case Studies | Maher Kayal |

THURSDAY, August 29

8:30-10:00 am	Digitally Controlled DC-DC Converters	Pavan Hanumolu
10:30-12:00 am	LED Drivers Design	Pavan Hanumolu
1:30-5:00 pm	The Design of Band-Gap References, Parts 1 & 2	Barrie Gilbert

FRIDAY, August 30

8:30-10:00 am	Nanopower Design Techniques and Efficient Energy Harvesting	Vadim Ivanov
10:30-12:00 am	Design of LDO's with instant Load Regulation and Unconditional Stability	Vadim Ivanov
1:30-3:00 pm	Circuit Techniques for Integrated Switching Regulators	Vadim Ivanov