Power Management

EPFL, LAUSANNE, SWITZERLAND
AUGUST 24-28, 2015

MONDAY, August 24

8:30-12:00 am	DC-DC Converters, Topologies & Control Techniques	Richard Redl
1:30-3:00 pm	DC-DC Converters, Topologies & Control Techniques	Richard Redl
3:30-5:00 pm	Converter Modeling and Feedback Loop Design	Richard Redl

TUESDAY, August 25

8:30-10:00 am	Control Techniques and their IC Implementations for Switched-Mode Converters in Portable Applications	Eduard Alarcon
10:30-12:00 pm	Adaptive Power Management Techniques for Portable Applications	Eduard Alarcon
1:30-5:00 pm	Battery Charging Techniques & Circuits for Notebook Computers & Cellular Phones	Thomas Szepesi

WEDNESDAY, August 26

8:30-12:00 am	DC-DC: From Discrete To Fully CMOS Integrated	Michiel Steyaert
1:30-5:00 pm	Linear Regulation Concepts & Review of Basic Topologies	Maher Kayal

THURSDAY, August 27

8:30-12:00 am	Classical Band-Gap References & Advances in Band-Gap Design	Barrie Gilbert
1:30-3:00 pm	Design of LDO's with instant Load Regulation and Unconditional Stability	Vadim Ivanov
3:30-5:00 pm	Circuit Techniques for Integrated Switching Regulators	Vadim Ivanov

FRIDAY, August 28

8:30-10:00 am	Nanopower Design Techniques and Efficient Energy Harvesting	Vadim Ivanov
10:30-12:00 am	Digitally Controlled DC-DC Converters	Pavan Hanumolu
1:30-3:00 pm	LED Drivers Design	Pavan Hanumolu