



Power Management

**EPFL, LAUSANNE, SWITZERLAND
AUGUST 28 – SEPTEMBER 1, 2017**

MONDAY, August 28

8:30-12:00 am & 1:30-3:00 pm	DC-DC Converters, Topologies & Control Techniques	Richard Redl
3:30-5:00 pm	Converter Modeling and Feedback Loop Design	Richard Redl

TUESDAY, August 29

8:30-12:00 am	Ultra-Low-Power Conversion, Harvesting	Eduard Alarcon
1:30-5:00 pm	Battery Charging Techniques & Circuits for Notebook Computers & Cellular Phones	Thomas Szepesi

WEDNESDAY, August 30

8:30-10:00 am	Power Stages and Gate Drivers	Bernhard Wicht
10:30-12:00 pm	Fundamentals of Linear Regulators	Pavan Hanumolu
1:30-3:00 pm	LED Drivers Design	Pavan Hanumolu
3:30-5:00 pm	Bandgap Voltage References	Willy Sansen

THURSDAY, August 31

8:30-12:00 am	DC-DC: From Discrete To Fully CMOS Integrated	Michiel Steyaert
1:30-3:00 pm	Digitally Controlled DC-DC Converters	Pavan Hanumolu
3:30-5:00 pm	Time-Based Control of DC-DC Converters	Pavan Hanumolu

FRIDAY, September 1

8:30-10:00 am	Design of LDO's with instant Load Regulation and Unconditional Stability	Vadim Ivanov
10:30-12:00 am	Circuit Techniques for Integrated Switching Regulators	Vadim Ivanov
1:30-3:00 pm	Nanopower Design Techniques and Efficient Energy Harvesting	Vadim Ivanov
