



MEAD Education S.A.

Practical Aspects in Mixed-Signal ICs

EPFL, LAUSANNE, SWITZERLAND

AUGUST 25-29, 2014

MONDAY, August 25

8:30 - 10:00 am	Trade-off between Analog and Digital - The Impact of Technology Scaling	Jan Rabaey
10:30 - 12:00 am	Ultra Low-Power Mixed Signal Interfaces	Jan Rabaey
1:30 - 5:00 pm	Digitally-Assisted Analog Circuits	Marc Pastre

TUESDAY, August 26

8:30 - 12:00 am	Noise Coupling in Mixed-Mode ICs: Mechanisms	Tim Schmerbeck
1:30 - 3:00 pm	Noise Coupling in Mixed-Mode ICs: Simulation/ Measurement	Tim Schmerbeck
3:30 - 3:00 pm	Noise Coupling in Mixed-Mode ICs: Design Strategy/Hardware Example	Tim Schmerbeck
3:30 - 5:00 pm	Design for Electrostatic Discharge (ESD) Robustness in Silicon Integrated Circuits	Tim Schmerbeck

WEDNESDAY, August 27

8:30 - 10:00 am	Offset and CMRR: Random and Systematic	Willy Sansen
10:30 - 12:00 am	Fully-Differential Amplifiers	Willy Sansen
1:30 - 3:00 pm	Interference Effects: CMRR/PSRR	Michiel Steyaert
3:30 - 5:00 pm	Design for EMC	Michiel Steyaert

THURSDAY, August 28

8:30 - 10:00 am	Nonideal Effects in Switched-Capacitor Circuits	Gabor Temes
10:30 - 12:00 am	Noise Calculation and Simulation in SC & CT Circuits	Christian Enz
1:30 - 3:00 pm	Noise and Offset Reduction Techniques	Christian Enz
3:30 - 5:00 pm	Matching Impairments in Mixed-Mode ICs	Herman Casier

FRIDAY, August 29

8:30 - 10:00 am	Matching Impairments in Mixed-Mode ICs (Cont.)	Herman Casier
10:30 - 3:00 pm	The Design of Band-Gap References, Parts 1 & 2	Barrie Gilbert
