



MEAD *Microelectronics, Inc.*

Practical Aspects in Mixed-Signal ICs

UNIVERSITY OF SANTA CRUZ, CALIFORNIA
MARCH 23-27, 2015

MONDAY, March 23

8:30 - 10:00 am	Noise Coupling in Mixed-Mode ICs: Mechanisms	Tim Schmerbeck
10:30 - 12:00 am	Noise Coupling in Mixed-Mode ICs: Simulation/ Measurement	Tim Schmerbeck
2:00 - 3:30 pm	Noise Coupling in Mixed-Mode ICs: Design Strategy/Hardware Example	Tim Schmerbeck
3:45 - 5:15 pm	ESD & EMC Robustness in Silicon ICs	Tim Schmerbeck

TUESDAY, March 24

8:30 - 10:00 am	ESD & EMC Robustness in Silicon ICs	Tim Schmerbeck
10:30 - 12:00 am	Beyond the Op Amp	Barrie Gilbert
2:00 - 5:15 pm	The Design of Band-Gap References	Barrie Gilbert

WEDNESDAY, March 25

8:30 - 12:00 am	Nonideal Effects in Switched-Capacitor Circuits	Gabor Temes
2:00 - 3:30 pm	Fully-Differential Amplifiers	Pavan Hanumolu
3:45 - 5:15 pm	Interference Effects: Offset/CMRR/PSRR	Pavan Hanumolu

THURSDAY, March 26

8:30 - 10:00 am	Offset and 1/f Noise Reduction Techniques	Boris Murmann
10:30 - 12:00 am	Noise Analysis and Simulation in SC Circuits	Boris Murmann
2:00 - 5:15 pm	G_m/I_D -based Design of Amplifier Circuits	Boris Murmann

FRIDAY, March 27

8:30 - 10:00 am	Trade-off between Analog and Digital - The Impact of Technology Scaling	Jan Rabaey
10:30 - 12:00 am	Ultra Low-Power Mixed Signal Interfaces	Jan Rabaey
