



RF Analog IC Design

EPFL PREMISES, LAUSANNE, SWITZERLAND
JUNE 28 - JULY 2, 2010

MONDAY, JUNE 28

8:30 - 12:00 am	Complex Circuits for RF Signal Processing	Andrew Teetzel
1:30 - 5:00 pm	CMOS Power Amplifiers	Ali Hajimiri

TUESDAY, JUNE 29

8:30 - 10:00 am	CMOS Down Converter Mixers	Michiel Steyaert
10:30 - 12:00 am	CMOS LNA Circuits	Michiel Steyaert
1:30 - 3:00 pm	CMOS Up Converter Mixers	Michiel Steyaert
3:30 - 5:00 pm	Active and Passive CMOS Mixers	John Cowles

WEDNESDAY, JUNE 30

8:30 - 12:00 am	MOS Transistor Modeling for RF IC Design	Christian Enz
1:30 - 3:00 pm	Direct-Conversion Radio Transceivers	Frank Op't Eynde
3:30 - 5:00 pm	Practical Design Constraints for RF ICs	Frank Op't Eynde

THURSDAY, JULY 1

8:30 - 12:00 am	High Speed Synthesizers for Communications	John Cowles
1:30 - 5:00 pm	Fractional-N PLLs for Frequency Synthesis	Ian Galton

FRIDAY, JULY 2

8:30 - 10:00 am	Passive Components and RF IC Design	John Long
10:30 - 12:00 am	ESD Protection and Packaging of RF Circuits	John Long
1:30 - 3:00 pm	Multi-band/Multi-standard Transceiver Circuits	John Long
