



MEAD Education S.A.

RF ANALOG IC DESIGN

**EPFL, LAUSANNE, SWITZERLAND
JUNE 24-28, 2013**

MONDAY, June 24

8:30 - 12:00 am	Complex Circuits for RF Signal Processing	Andy Teetzel
1:30-5:00 pm	Wireless CMOS Receiver Front-End Circuits and Systems	Michiel Steyaert

TUESDAY, June 25

8:30-12:00 am	Fractional-N PLLs for Frequency Synthesis	Ian Galton
1:30-5:00 pm	CMOS Power Amplifiers	Ali Hajimiri

WEDNESDAY, June 26

8:30-10:00 am	High Speed Synthesizers for Communications: Block Level Design	John Cowles
10:30-12:00 am	Layout for RF Circuits	John Cowles
1:30-3:00 pm	Direct-Conversion Radio Transceivers	Frank Op't Eynde
3:30-5:00 pm	Introduction to All-Digital Synthesizers, Receivers and Transmitters	Frank Op't Eynde

THURSDAY, June 27

8:30-12:00 am	CMOS LNA and Mixer Design	Behzad Razavi
1:30-5:00 pm	MOS Transistor Modeling for RF IC Design	Christian Enz

FRIDAY, June 28

8:30-12:00 am	Passive Components and RF IC Design	John Long
1:30-3:00 pm	ESD Protection and Packaging of RF Circuits	John Long
