



## Ultra-Low Voltage Design

EPFL PREMISES, LAUSANNE, SWITZERLAND  
AUGUST 23-27, 2010

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### MONDAY, AUGUST 23

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8:30 - 12:00 am	Ultra-Low Voltage Analog Circuits	Christian Enz
1:30 - 5:00 pm	Ultra Low-Voltage and Low-Power Digital Design	Jan Rabaey

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### TUESDAY, AUGUST 24

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8:30 - 10:00 am	Ultra-Low Voltage Amplifiers	Willy Sansen
10:30 - 12:00 am	Ultra-Low Voltage Filters	Willy Sansen
1:30 - 3:00 pm	Weak Inversion for Ultimate Low-Power Logic	Eric Vittoz
3:30 - 5:00 pm	<b>Panel Session: Future Challenges for Mixed-Signal Designer</b>	

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### WEDNESDAY, AUGUST 25

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8:30 - 10:00 am	Nanoscale CMOS Device Matching and Precision Element Matching for Filters and ADCs	Lanny Lewyn
10:30 - 12:00 am	Nanoscale CMOS Physical Design Problems & Solutions	Lanny Lewyn
1:30 - 5:00 pm	Micro-Power Data Converters	Gabor Temes

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