Techniques for Handling Noise and Variability in Analog Circuits

EPFL, LAUSANNE, SWITZERLAND
JANUARY 21-25, 2019

MONDAY, January 21

8:30-12:00 am	Random Mismatch Origins	Marcel Pelgrom
1:30-5:00 pm	Analyzing Mismatch and Yield in Analog Circuits	Marcel Pelgrom

TUESDAY, January 22

8:30-12:00 am	Fundamentals of Noise in Electronic Devices	Christian Enz
1:30-5:00 pm	Layout Strategies to Reduce Offset	Marcel Pelgrom

WEDNESDAY, January 23

8:30-10:00 am	Offset, CMRR and PSRR	Willy Sansen
10:30-12:00 am	Variability in Bandgaps	Willy Sansen
1:30-5:00 pm	Dynamic Offset-Cancellation Techniques	Kofi Makinwa

THURSDAY, January 24

8:30-12:00 am	Dynamic Element Matching Techniques	Kofi Makinwa
1:30-3:00 pm	Noise Cancellation Techniques	Willy Sansen
3:30-5:00 pm	Noise Sampling in Switched Capacitor Filters	Willy Sansen

FRIDAY, January 25

8:30-12:00 am & 1:30-3:00 pm	Noise Analysis in Continuous-Time and Sampled-Data Circuits	Christian Enz