



# Techniques for Handling Noise and Variability in Analog Circuits

**EPFL, LAUSANNE, SWITZERLAND  
JANUARY 25-29, 2021**

---

## **MONDAY, January 25**

---

8:30-12:00 pm	Random Mismatch Origins	Marcel Pelgrom
1:30-5:00 pm	Analyzing Mismatch and Yield in Analog Circuits	Marcel Pelgrom

---

## **TUESDAY, January 26**

---

8:30-12:00 pm	Layout Strategies to Reduce Offset	Marcel Pelgrom
1:30-3:00 pm	Offset, CMRR and PSRR	Willy Sansen
3:30-5:00 pm	Variability in Bandgaps	Willy Sansen

---

## **WEDNESDAY, January 27**

---

8:30-12:00 pm	Fundamentals of Noise in Electronic Devices	Christian Enz
1:30-3:00 pm	Noise Cancellation Techniques	Willy Sansen
3:30-5:00 pm	Noise Sampling in Switched Capacitor Filters	Willy Sansen

---

## **THURSDAY, January 28**

---

8:30-12:00 pm & 1:30-3:00 pm	Noise Analysis in Continuous-Time and Sampled-Data Circuits	Christian Enz
3:30-5:00 pm	Dynamic Offset-Cancellation Techniques	Kofi Makinwa

---

## **FRIDAY, January 29**

---

8:30-10:00 am	Dynamic Offset-Cancellation Techniques	Kofi Makinwa
1:30-12:00 pm & 1:30-3:00 pm	Dynamic Element Matching Techniques	Kofi Makinwa

---