



Wireless RF Front-End Design: from ULP to mmW

EPFL, LAUSANNE, SWITZERLAND
JUNE 19-23, 2017

MONDAY, June 19

8:30-10:00 am	Characteristics of Ultra-Low Power Systems	Antonio Liscidini
10:30-12:00 am	Introduction to Wireless RX and TX	Antonio Liscidini
1:30-3:00 pm	In-Sight Some Ultra-Low Power Standards	Antonio Liscidini
3:30-5:00 pm	Transceiver Architectures, Design Considerations	Antonio Liscidini

TUESDAY, June 20

8:30-10:00 am	Low Noise Amplifiers	Antonio Liscidini
10:30-12:00 am	Mixers, Base-Band Filters	Antonio Liscidini
1:30-3:00 pm	Power Amplifiers, Frequency Generation	Antonio Liscidini
3:30-5:00 pm	Power Scalability, Bias Sharing	Antonio Liscidini

WEDNESDAY, June 21

8:30-12:00 am	Actives and Passives at mm-Wave Frequencies	Patrick Reynaert
1:30-3:00 pm	VCOs and Dividers	Patrick Reynaert
3:30-5:00 pm	LNAs and Receivers	Patrick Reynaert

THURSDAY, June 22

8:30-12:00 am & 1:30-5:00 pm	CMOS mm-Wave Power Amplifiers and Transmitters	Patrick Reynaert
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FRIDAY, June 23

8:30-12:00 am	CMOS mm-Wave and sub-THz Examples	Patrick Reynaert
1:30-3:00 pm	RF Transceiver Design - Practical Aspects	Paul Muller
