



# Wireless RF Front-End Design: from ULP to mmW

**EPFL, LAUSANNE, SWITZERLAND  
JUNE 18-22, 2018**

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## **MONDAY, June 18**

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8:30-10:00 am	Characteristics of Ultra-Low Power Systems	Antonio Liscidini
10:30-12:00 am	Introduction to Wireless RX and TX	Antonio Liscidini
1:30-3:00 pm	In-Sight Some Ultra-Low Power Standards	Antonio Liscidini
3:30-5:00 pm	Transceiver Architectures, Design Considerations	Antonio Liscidini

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## **TUESDAY, June 19**

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8:30-10:00 am	Low Noise Amplifiers	Antonio Liscidini
10:30-12:00 am	Mixers, Base-Band Filters	Antonio Liscidini
1:30-3:00 pm	Power Amplifiers, Frequency Generation	Antonio Liscidini
3:30-5:00 pm	Power Scalability, Bias Sharing	Antonio Liscidini

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## **WEDNESDAY, June 20**

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8:30-10:00 am	Actives and Passives at mm-Wave Frequencies	Patrick Reynaert
10:30-12:00 am	Fundamental Trade-off for mm-Wave Circuits	Patrick Reynaert
1:30-3:00 pm	Broadband mm-Wave Design Techniques	Patrick Reynaert
3:30-5:00 pm	mm-Wave PAs: Basic Trade-offs and Limits	Patrick Reynaert

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## **THURSDAY, June 21**

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8:30-10:00 am	mm-Wave PAs: Circuits and Architectures	Patrick Reynaert
10:30-12:00 am	mm-Wave PAs: Design Examples	Patrick Reynaert
1:30-5:00 pm	Fractional N-PLLs for Frequency Synthesis	Ian Galton

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## **FRIDAY, June 22**

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8:30-10:00 am	mm-Wave Communication Links	Patrick Reynaert
10:30-12:00 am	Towards THz Circuits in CMOS	Patrick Reynaert
1:30-3:00 pm	RF Transceiver Design - Practical Aspects	Paul Muller

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