

Basics in RF IC Design

ON-LINE CLASS on Microsoft TEAMS

June 23 - July 4, 2025

WEEK 1	JUNE 23-27			
WEEK 2	JUNE 30 - JULY 4			
DAILY	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
	CET (Lausanne)	EST (New York)	PST (California)	IST (India)
Module 1	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	6:30-8:00 pm
Module 2	5:00-6:30 pm	11:00am-12:30 pm	8:00-9:30 am	8:30-10:00 pm
WEEK 1	Module			
Day 1, Monday, June 23	1	Introduction to Wireless RX and TX		Antonio Liscidini
	2	LNA Topologies		Antonio Liscidini
Day 2, Tuesday, June 24	1	Integrated Mixers		Antonio Liscidini
	2	Base Band Filters		Antonio Liscidini
Day 3, Wednesday, June 25	1	Wireless CMOS Receiver Front-End Systems		Michiel Steyaert
	2	Wireless CMOS Receiver Front-End Circuits		Michiel Steyaert
Day 4, Thursday, June 26	1	RF CMOS LNA Topologies		Michiel Steyaert
	2	Up-Converters and Single Chip Transceivers		Michiel Steyaert
Day 5, Friday, June 27	1	Fundamentals of Analog PLLs		Michiel Steyaert
	2	Interference Effects in PLLs		Michiel Steyaert
WEEK 2	Module			
Day 6, Monday, June 30	1	PLL Building Blocks		Sam Palermo
	2	PLL Analysis and Modeling		Sam Palermo
Day 7, Tuesday, July 1	1&2	Spiral Inductor Interference, Deadzone and Phase Noise		Michiel Steyaert
Day 8, Wednesday, July 2	1	Integrated Power Amplifiers (PA): System Level		Patrick Reynaert
	2	Integrated PA: Classes and Topologies		Patrick Reynaert
Day 9, Thursday, July 3	1	Integrated PA: Design and Implementation		Patrick Reynaert
	2	Integrated PA: Stability and Layout		Patrick Reynaert
Day 10, Friday, July 4	1	Integrated PA: Design at mm-Wave Frequencies		Patrick Reynaert
	2	Integrated PA: Broadband Design with Transformers		Patrick Reynaert