LOW-POWER ANALOG IC DESIGN
ON-LINE CLASS
SEPTEMBER 21 - OCTOBER 2, 2020

WEEK 1		SEP 21-25	10 Modules (1:30hr each), 2 Modules per day		
WEEK 2		SEPT 28 - OCT 2	10 Modules (1:30hr each), 2 Modules per day		
		Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
DAILY		**CET (Lausanne)**	**EST (New York)**	**PST (California)**	**IST (India)**
Module 1		3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	6:30-8:00 pm
Module 2		5:00-6:30 pm	11:00 am -12:30 pm	8:00-9:30 am	8:30-10:00 pm
WEEK 1	Module				
Monday, Sept 21	1&2	MOS Transistor Modeling for Low-Voltage Low-Power Circuit Design			Christian Enz
Tuesday, Sept 22	1	Basic low-Power low-Voltage Circuit Techniques			Willy Sansen
	2	Differential Amplifying Blocks with Positive Feedback			Willy Sansen
Wednesday, Sept 23	1	Noise Performance of Elementary Transistor Stages			Willy Sansen
	2	Stability of Operational Amplifiers			Willy Sansen
Thursday, Sept 24	1	Systematic Design of Low-Power Operational Amplifiers			Willy Sansen
	2	Important Opamp Configurations			Willy Sansen
Friday, Sept 25	1	Fully-Differential Operational Amplifiers			Willy Sansen
	2	Bandgap and Current Reference Circuits			Willy Sansen
WEEK 2					
Monday, Sept 28	1&2	Micropower ADCs			Kofi Makinwa
Tuesday, Sept 29	1	Distortion in Elementary Transistor Circuits			Willy Sansen
	2	Low-Power Continuous-Time Filters			Willy Sansen
Wednesday, Sept 30	1&2	Matching of MOS Transistors in Deep-Submicron			Marcel Pelgrom
Thursday, Oct 1	1&2	Layout Considerations in Mixed-Signal Circuit Design			Marcel Pelgrom
Friday, Oct 2	1&2	Design of Low-power Analog Circuits using the Inversion Coefficient			Christian Enz