



LOW-POWER ANALOG IC DESIGN

ON-LINE CLASS on MS TEAMS

JUNE 26 - JULY 7, 2023

WEEK 1		JUNE 26-30			
WEEK 2		JULY 3-7			
DAILY	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time	
	CET (Lausanne)	EST (New York)	PST (California)	IST (India)	
Module 1	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	6:30-8:00 pm	
Module 2	5:00-6:30 pm	11:00 - 12:30 am	8:00-9:30 am	8:30-10:00pm	
WEEK 1	Module				
Monday, June 26	1&2	MOS Transistor Modeling for Low-Voltage and Low-Power Circuit Design			Christian Enz
Tuesday, June 27	1	Basic Low-Power Low-Voltage Circuit Techniques			Willy Sansen
	2	Differential Amplifying Blocks with Positive Feedback			Willy Sansen
Wednesday, June 28	1	Noise Performance of Elementary Transistor Stages			Willy Sansen
	2	Stability of Operational Amplifiers			Willy Sansen
Thursday, June 29	1	Systematic Design of Low-Power Operational Amplifiers			Willy Sansen
	2	Important Opamp Configurations			Willy Sansen
Friday, June 30	1	Fully-Differential Operational Amplifiers			Willy Sansen
	2	Bandgap and Current Reference Circuits			Willy Sansen
WEEK 2	Module				
Monday, July 3	1&2	Design of Low-Power Analog Circuits using the Inversion Coefficient			Christian Enz
Tuesday, July 4	1&2	Micropower ADCs			Kofi Makinwa
Wednesday, July 5	1	Distortion in Elementary Transistor Circuits			Willy Sansen
	2	Low-Power Continuous-Time Filters			Willy Sansen
Thursday, July 6	1&2	Frequency Generation Circuits			Taekwang Jang
Friday, July 7	1	Structural Design Methodology and Practical Frequency Compensation IOT			Vadim Ivanov
	2	Nanopower Design Techniques & Efficient Energy Harvesting			Vadim Ivanov