



Low-Power Analog IC Design

ON-LINE CLASS by Microsoft TEAMS

September 13 - 24, 2021

WEEK 1	SEPT 13-17			
WEEK 2	SEPT 20 -24			
	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
DAILY	CET (Lausanne)	EST (New York)	PST (California)	IST (India)
Module 1	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	6:30-8:00 pm
Module 2	5:00-6:30 pm	11:00 -12:30 am	8:00-9:30 am	8:30-10:00 pm
WEEK 1	Module			
Monday, Sept 13	1&2	MOS Transistor Modeling for Low-Voltage Low-Power Circuit Design		Christian Enz
Tuesday, Sept 14	1	Basic low-Power low-Voltage Circuit Techniques		Willy Sansen
	2	Differential Amplifying Blocks with Positive Feedback		Willy Sansen
Wednesday, Sept 15	1	Noise Performance of Elementary Transistor Stages		Willy Sansen
	2	Stability of Operational Amplifiers		Willy Sansen
Thursday, Sept 16	1	Systematic Design of Low-Power Operational Amplifiers		Willy Sansen
	2	Important Opamp Configurations		Willy Sansen
Friday, Sept 17	1	Fully-Differential Operational Amplifiers		Willy Sansen
	2	Bandgap and Current Reference Circuits		Willy Sansen
WEEK 2				
Monday, Sept 20	1&2	Design of Low-power Analog Circuits using the Inversion Coefficient		Christian Enz
Tuesday, Sept 21	1	Micropower ADCs		Kofi Makinwa
	2	Distortion in Elementary Transistor Circuits		Willy Sansen
Wednesday, Sept 22	1&2	Low-Power Continuous-Time Filters		Willy Sansen
Thursday, Sept 23	1&2	Matching of MOS Transistors in Deep-Submicron		Marcel Pelgrom
Friday, sept 24	1&2	Layout Considerations in Mixed-Signal Circuit Design		Marcel Pelgrom