



Delta-Sigma Data Converters

ON-LINE CLASS on Microsoft TEAMS

July 1-12, 2024

WEEK 1		JULY 1-5, 2024			
WEEK 2		JULY 8-12, 2024			
DAILY	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time	
	CET (Lausanne)	EST (New York)	PST (California)	IST (India)	
Module 1	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	6:30-8:00 pm	
Module 2	5:00-6:30 pm	11:00 am-12:30 pm	8:00-9:30 am	8:30-10:00 pm	
WEEK 1	Module				
Monday, July 1	1 & 2	Delta Sigma Converter Basics – Parts A & B			Shanthi Pavan
Tuesday, July 2	1 & 2	Delta Sigma Converter Basics – Parts C & D			Shanthi Pavan
Wednesday, July 3	1	Delta Sigma Converter Basics – Part E			Shanthi Pavan
	2	High-Level Design of CT-Delta Sigma Modulators			Shanthi Pavan
Thursday, July 4	1	Discrete-Time Delta Sigma Design			David Johns
	2	Introduction to the Delta Sigma Toolbox			David Johns
Friday, July 5	1	Bandpass Delta Sigma ADCs			David Johns
	2	Incremental and Sensor ADCs			David Johns
WEEK 2	Module				
Monday, July 8	1	Circuit Techniques to Mitigate Flicker Noise in CTDS Modulators			Shanthi Pavan
	2	Non-Idealities in Continuous-Time DS Modulators			Shanthi Pavan
Tuesday, July 9	1	Design of Building Blocks for CTDSM Modulators - part 1			Shanthi Pavan
	2	Design of Building Blocks for CTDSM Modulators - part 2			Shanthi Pavan
Wednesday, July 10	1	Systematic Design Centering of a Practical CTDSM			Shanthi Pavan
	2	FIR feedback in CTDSMs			Shanthi Pavan
Thursday, July 11	1	Introduction to Dynamic Element Matching and Calibration			Shanthi Pavan
	2	Cascaded Continuous-Time Delta-Sigma Converters			Shanthi Pavan
Friday, July 12	1	Case Studies : Part 1			Shanthi Pavan
	2	Case Studies : Part 2			Shanthi Pavan