



RF Circuit Design

ON-LINE CLASS on Microsoft TEAMS

May 6-17, 2024

WEEK 1	MAY 6-10, 2024			
WEEK 2	MAY 13-17, 2024			
DAILY	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
	CET (Lausanne)	EST (New York)	PST (California)	IST (India)
Module 1	3:00-4:30 pm	9:00-10:30 am	6:00-7:30 am	6:30-8:00 pm
Module 2	5:00-6:30 pm	11:00am-12:30 pm	8:00-9:30 am	8:30-10:00 pm
WEEK 1	Module			
Day 1, Monday, May 6	1	Introduction to Wireless RX and TX		Antonio Liscidini
	2	Low Noise Amplifiers		Antonio Liscidini
Day 2, Tuesday, May 7	1	Frequency Generation		Antonio Liscidini
	2	PLL Overview		Antonio Liscidini
Day 3, Wednesday, May 8	1	Mixers		Antonio Liscidini
	2	Base-Band Filters		Antonio Liscidini
Day 4, Thursday, May 9	1	Receiver Architectures, Design Considerations		Antonio Liscidini
	2	Transmitter Architectures, Design Considerations		Antonio Liscidini
Day 5, Friday, May 10	1	Power Amplifiers: System Level		Patrick Reynaert
	2	Power Amplifiers: Circuit Level		Patrick Reynaert
WEEK 2	Module			
Day 6, Monday, May 13	1	mm-Wave Circuit Design: Actives		Patrick Reynaert
	2	mm-Wave Circuit Design: Passives		Patrick Reynaert
Day 7, Tuesday, May 14	1	mm-Wave Circuit Design: Transformers		Patrick Reynaert
	2	mm-Wave Circuit Design: Examples		Patrick Reynaert
Day 8, Wednesday, May 15	1	mm-Wave Circuit Design: Phased Array Fundamentals		Bodhisatwa Sadhu
	2	mm-Wave Circuit Design: Phased Array Architectures & Scaling		Bodhisatwa Sadhu
Day 9, Thursday, May 16	1	mm-Wave Circuit Design: Phased Array Circuits		Bodhisatwa Sadhu
	2	mm-Wave Circuit Design: Phased Array Module Integration		Bodhisatwa Sadhu
Day 10, Friday, May 17	1	5G mm-Wave Transmitter Array Design Examples		Hua Wang
	2	5G Digital Power Amplifiers and Transmitters		Hua Wang