



Wireline SERDES Transceivers

ON-LINE CLASS by Microsoft TEAMS

May 8-19, 2023

WEEK 1	MAY 8-12, 2023			
WEEK 2	MAY 15-19, 2023			
DAILY	Central European Time	Eastern Standard Time	Pacific Standard Time	India Standard Time
	CET (Lausanne)	EST (New York)	PST (California)	IST (India)
Module 1	4:00-5:30 pm	10:00-11:30 am	7:00-8:30 am	7:30-9:00 pm
Module 2	6:00-7:30 pm	12:00-1:30 pm	9:00-10:30 am	9:30-11:00 pm
WEEK 1	<i>Module</i>			
Monday, May 8	1	Introduction to Wireline Transceivers		Pavan Hanumolu
	2	Transmitters (CML/VM)		Pavan Hanumolu
Tuesday, May 9	1	FIR Equalizers (Tx/Rx)		Pavan Hanumolu
	2	CTLE		Pavan Hanumolu
Wednesday, May 10	1	DFE, Adaptation		Pavan Hanumolu
	2	Phase-Locked Loops		Pavan Hanumolu
Thursday, May 11	1	Advanced PLLs		Pavan Hanumolu
	2	Clock and Data Recovery		Pavan Hanumolu
Friday, May 12	1	Phase/Frequency Detectors		Pavan Hanumolu
	2	Advanced CDRs		Pavan Hanumolu
WEEK 2	<i>Module</i>			
Monday, May 15	1	Baud-Rate CDRs		Pavan Hanumolu
	2	Trans-Impedance Amplifiers		Pavan Hanumolu
Tuesday, May 16	1	Advanced Signaling Methods		Armin Tajalli
	2	Short Reach Transceiver Design Tradeoffs		Armin Tajalli
Wednesday, May 17	1	Tradeoffs in Design of Slicers		Armin Tajalli
	2	Discrete-Time Front-End Design		Armin Tajalli
Thursday, May 18	1	Optical Transmitters		Sam Palermo
	2	ADC-Based RX Analysis and Digital Equalization		Sam Palermo
Friday, May 19	1	Wireline RX Time-Interleaved ADC Design and Calibration		Sam Palermo
	2	DSP-DAC Wireline Transmitters		Sam Palermo