Advanced Analog CMOS IC Design

**EPFL, LAUSANNE, SWITZERLAND
AUGUST 28 – SEPTEMBER 1, 2017**

MONDAY, August 28

8:30-12:00 am	MOS and Bipolar: Modes of Operation and Models	Eric Vittoz
1:30-3:00 pm	Elementary Building Blocks	Eric Vittoz
3:30 -5:00 pm	Voltage References	Eric Vittoz

TUESDAY, August 29

8:30-12:00 am & 1:30-5:00 pm	Analog Functional Blocks	Willy Sansen

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WEDNESDAY, August 30

8:30-12:00 am	Continuous Time Filters	Boris Murmann
1:30-5:00 pm	Switched-Capacitor Interface Circuits	Boris Murmann

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THURSDAY, August 31

8:30-10:00 am	Switched-Capacitor Interface Circuits	Boris Murmann
10:30-12:00 am & 1:30 - 5:00 pm	CMOS Off-Chip Drivers	Rinaldo Castello

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FRIDAY, September 1

8:30-12:00 am & 1:30 - 3:00 pm	Technology and other Limitations for Analog Design	Herman Casier